PENDING CLAIMS AS AMENDED

Please amend the claims as follows:

1.   (Original) A method of parallel programming an electronic device's memory with test code and system code prior to board level testing during manufacturing, the method comprising the steps of:

programming said electronic device with first instructions and second instructions, wherein said first instructions comprise said test code for use during board level testing of said electronic device, and wherein said second instructions comprise partial system code for system level testing of said electronic device;

executing said first instructions during board level testing of said electronic device to determine the condition of said electronic device independent of said second instructions;

programming said electronic device with third instructions, wherein said third instructions include system code to complement said second instructions; and

executing said second instructions and said third instructions during system level testing of said electronic device.

2.   (Withdrawn) The method of Claim 1, wherein said memory location is ROM memory.

3.   (Withdrawn) The method of Claim 1, wherein said memory location is RAM memory.

4.   (Original) The method of Claim 1, wherein said memory location is flash memory.

5.   (Original) The method of Claim 1, wherein said step of programming said electronic device with said third instructions comprises overwriting said first instructions with said second instructions.

6.   (Currently amended) The method of Claim 1, wherein said third instructions are [U/I] user interface codes.

7. (Original) The method of Claim 1, further comprising the step of programming fourth instructions, wherein said fourth instructions include customized code for an individual user.

8. (Withdrawn) The method of Claim 1, wherein said third instructions are unique to a single mode cellular phone.

9. (Withdrawn) The method of Claim 1, wherein said third instructions are unique to a dual mode cellular phone.

10. (Withdrawn) The method of Claim 1, wherein said third instructions are unique to a tri-mode cellular phone.

11. (Original) A system for parallel programming an electronic device's memory with test code and system code prior to board level testing during manufacturing, the system comprising:

means for programming said electronic device with first instructions and second instructions, wherein said first instructions comprise said test code for use during board level testing of said electronic device, and wherein said second instructions comprise partial system code for system level testing of said electronic device;

means for executing said first instructions during board level testing of said electronic device to determine the condition of said electronic device independent of said second instructions;

means for programming said electronic device with third instructions, wherein said third instructions complement said second instructions to complete said system code; and

means for executing said second instructions and said third instructions during system level testing of said electronic device.

12. (Original) A system for parallel programming an electronic device's memory with test code and system code prior to board level testing during manufacturing, the system comprising:

an electronic device;

a programmable memory located in said electronic device;

a first input device in communication with said electronic device, wherein first instructions and second instructions are programmed into said programmable memory by said input device, and wherein said first instructions comprise said test code and said second instructions comprise said partial system code;

means for executing said first instructions stored in said electronic device;

a second input device in communication with said electronic device, wherein third instructions are programmed into said programmable memory to complement said second instructions to complete said system code; and

means for executing said second instructions and said third instructions stored in said electronic device during system level testing of said electronic device.

13.   (Original) The system of Claim 12, wherein said first and second input devices are the same input device.

14.   (Original) The system of Claim 12, wherein said programmable memory is flash RAM.

15.   (Currently amended) The system of Claim 12, wherein said third instructions are [U/I] user interface codes.

16.   (Withdrawn) The system of Claim 12, wherein said electronic device is a cellular phone.

17.   (Withdrawn) The system of Claim 12, wherein said electronic device is a personal data assistant.

18.   (Withdrawn) The system of Claim 12, wherein said programmable memory is ROM memory

19.   (Withdrawn) The system of Claim 12, wherein said programmable memory is RAM memory.

20.   (Original) The system of Claim 12, wherein said programmable memory is flash memory.